

January 6, 2004

To: Commissioner for Patents

P.O.Box 1450

Alexandria, VA 22313-1450

Fr: George O. Saile, Reg. No. 19,572

28 Davis Avenue

Poughkeepsie, N.Y. 12603

Subject:

Serial No. 10/613,600 07/03/03

Jurgen Kernhof et al.

32V H-BRIDGE DRIVER WITH CMOS CIRCUITS

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO-1449, Information Disclosure Citation
In An Application.

The following Patents and/or Publications are submitted to comply with the duty of disclosure under CFR 1.97-1.99 and 37 CFR 1.56.

## CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on January 12, 2004.

Stephen B. Ackerman, Reg.# 37761

Signature/Date

1/13/07

DS-02-026

Information in this Information Disclosure Statement was cited in a communication from a foreign Patent Office in a counterpart foreign application. The concise explanation of relevance for the non-English documents is provided therein. A copy of that communication is attached.

European Patent EP 1 193 824 A2, (Delphi Tech Inc.),
April 3, 2002, "Gegenspannungsschutzschaltung."

U.S. Patent 5,796,276 to Phillips et al., "High-Side-Driver Gate Drive Circuit," discloses a high-side gate driving circuit, where a current-mode differential error amplifier is used to regulate the current sourced to the gate.

DE 198 03 040 A 1 (Int Rectifier Corp), August 6, 1998, "Die folgenden Angaben sind den vom Anmelder eingereichten Unterlagen entnommem."

Sincerely,

tephen B. Ackerman,

Reg. No. 37761

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